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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/256,079	02/24/1999	YUMIKO KAWASAKI	837.1196/JDH	9353

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EXAMINER

TRAN, DZUNG D

ART UNIT PAPER NUMBER

2633

DATE MAILED: 12/21/2001

Please find below and/or attached an Office communication concerning this application or proceeding.

# Office Action Summary

Application No.

09/256,079

Applicant(s)

KAWASAKI ET AL.

Examiner

Dzung D Tran

Art Unit

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

## Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

- 1) ☒ Responsive to communication(s) filed on 24 February 1999.
- 2a) ☐ This action is FINAL.
- 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

## Disposition of Claims

- 4) ☒ Claim(s) 1-16 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-16 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

## Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on \_\_\_\_\_ is: a) ☐ approved b) ☐ disapproved by the Examiner.  
If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

## Priority under 35 U.S.C. §§ 119 and 120

- 13) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
a) ☐ All b) ☐ Some \* c) ☐ None of:  
1. ☒ Certified copies of the priority documents have been received.  
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).  
\* See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).  
a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

## Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449) Paper No(s) 2,4.

- 4) ☐ Interview Summary (PTO-413) Paper No(s). \_\_\_\_\_
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_\_

**DETAILED ACTION**

***Specification***

***Claim Rejections - 35 USC § 102***

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

2. Claims 1-5 and 16 are rejected under 35 U.S.C. 102(e) as being anticipated by Ishikawa U.S. patent no. 5,917,637.

In considering claim 1, Ishikawa et al. disclose a method comprising the step of: outputting an optical signal having a chirping determined by a chirp parameter to an optical fiber transmission line (column 2, line 37-39, column 3, lines 51-54 and column 6, lines 36-43), converting the optical signal transmitted by said optical fiber transmission line into an electrical signal (column 7, lines 33-35), detecting a bit error of said electrical signal (column 10, lines 9-14), and controlling said chirp parameter so that said bit error detected is reduced (column 2, lines 43-46, column 3, lines 59-65).

In considering claim 2, Ishikawa et al. disclose the step of switching the sign of chirp parameter (column 9, lines 41-49).

In considering claim 3, Ishikawa et al. further disclose Mach-Zehnder optical modulator and step of switching an operating point of Mach-Zehnder optical modulator (column 9, lines 41-49).

In considering claim 4, Ishikawa further discloses control unit for changing a bias voltage to be applied to electroabsorption optical modulator, thereby adjusting chirp parameter to an optimum value so that bit error detect is minimized ( column 2, lines 32-46).

In considering claim 5, Ishikawa et al. further disclose an electro-absorption optical modulator (column 1, lines 44-45, 55-56 and column 2, lines 33, 38 and 49).

In considering claim 16, Ishikawa et al. further disclose an optical transmitter for outputting an optical signal having a chirping determined by a chirp parameter to an optical fiber transmission line (column 2, lines 37-39, column 3, lines 51-54 and column 6, lines 36-43), an optical receiver (figure 12, element 68) for receive supervisory information on a bit error detected in relation to the optical signal transmitted by optical transmission line, and a control unit (figure 10, element 10) for controlling chirp parameter according to a control signal.

### ***Claim Rejections - 35 USC § 103***

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

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(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

4. Claims 7 and 11-15 are rejected under 35 U.S.C. 103(a) as being unpatentable over Ishikawa U.S. patent no. 5,917,637 in view of Aoki U.S. patent no. 5,315,426.

In considering claim 7, Ishikawa et al. disclose first terminal device (figure 12, element 66) and second terminal device (figure 12, element 70), and an optical fiber transmission line (figure 12, element 72) connecting said first and second terminal device. First terminal device comprising an optical transmitter for outputting an optical signal having a chirping determined by a chirp parameter to an optical fiber transmission line (column 2, lines 37-39, column 3, lines 51-54 and column 6, lines 36-43) and a control unit (figure 10, element 10) for controlling chirp parameter according to a control signal. Second terminal device comprising an optical receiver (figure 12, element 68), a monitor unit for detecting a bit error of electrical signal and for transmitting supervisory information to first terminal whereby control signal is generated in first terminal device so that bit error detected is reduced (column 9, line 66 to column 10, line 13). Ishikawa et al. differs from claim 7 of the present invention in that he does not specific disclose second terminal device comprising an optical receiver for converting the optical signal transmitted by said optical said optical fiber transmission line into an electrical signal. Aoki discloses an optical receiver for converting the optical signal transmitted by said optical said optical fiber transmission line into an electrical signal (figure 2, element 32). Since photodetector is well-known in the art for convert an optical signal to electrical

signal, it would have been obvious to an artisan at the time the invention was made to include a photo-detector in a receiver of Aoki in the receiver of Ishikawa in order to obtain the control electrical signals for detect a bit error signal and control the chirp parameter.

In considering claim 11, Aoki discloses a transmitter included an optical amplifier (figure 2, element 17 and column 4, line 23).

In considering claim 12, Aoki discloses a receiver included an optical amplifier (figure 2, element 33 and column 4, line 39).

In considering claim 13, Aoki discloses an optical transmission line is provided by a dispersion shifted fiber having a zero-dispersion wavelength near 1.55 m (column 4, lines 32-34).

In considering claim 14, Ishikawa discloses the fiber optic transmission path having a zero -dispersion wavelength within a 1.3 m band (column 10, line 13-15)

In considering claim 15, Onaka et al. disclose dispersion compensating fiber (figure 1, element 80 and column 3, line 48).

5. Claims 8 and 9 are rejected under 35 U.S.C. 103(a) as being unpatentable over Ishikawa et al. U.S. patent no. 5,917,637 in view of Aoki U.S. patent no. 5,315,426 and further in view of Brenner et al U. S. patent no. 6,115,403

In considering claim 8, as per claims above, Aoki and Ishikawa disclose all the limitations except for transmitter comprises a light source for outputting continuous wave (CW) light and a Mach-Zehnder modulator for modulating CW light to generate an optical signal. Brenner et al. disclose transmitter comprises a light source for outputting

continuous wave (CW) light and a modulator for modulating CW light to generate an optical signal (column 1, lines 16-20). Since the optical signals which are generated by operating a laser in a continuous-wave (CW) mode is well known in the art, it would have been obvious to an artisan at the time the invention was made to include the light source and Mach-Zehnder modulator taught by Brenner in the transmission system of Aoki and Ishikawa in order to emit a steady stream of laser light.

In considering claim 9, Ishikawa further discloses control unit for changing a bias voltage to be applied to electroabsorption optical modulator, thereby adjusting chirp parameter to an optimum value so that bit error detect is minimized (column 2, lines 32-46).

6. Claim 6 is rejected under 35 U.S.C. 103(a) as being unpatentable over Ishikawa et al. U.S. patent no. 5,917,637 in view of Alexander et al. U.S. patent no. 5,784,184 and Taga et al. U. S. patent no. 5,585,954

As per claim 1 above, Ishikawa discloses all the limitations except for the step of generating optical signal by optical modulation based on modulating signal obtain by adding a redundancy code to a transmission data code, the step of correcting bit error of electrical signal according to redundancy code and the step of counting the number of corrections of bit error.

Alexander et al. disclose an optical transmitter include an encoder for adding a redundancy code to a transmission data code (column 5 line 66 to column 6, line 7) and an optical receiver include a decoder for correcting bit error of electrical signal according to redundancy code (column 6, lines 7-12). It would have been obvious to an artisan at

the time the invention was made to include the encoder and decoder of Alexander in the transmission system of Ishikawa in order to monitor and correct the bit error rate (column 5 line 66 to column 6, line 12).

Taga et al. disclose counter (figure 2, element 14) for counting the number of correction of bit error obtained by decoder (column 4, line 64-67). Since bit error counter is well known for counting bit errors on the signal, it would have been obvious to an artisan at the time the invention was made to include the bit error counter of Taga in the system of Ishikawa and Alexander in order to measure and correct the bit error rate.

7. Claim 10 is rejected under 35 U.S.C. 103(a) as being unpatentable over Ishikawa et al. U.S. patent no. 5,917,637 in view of Aoki U.S. patent no. 5,315,426, Brenner et al. U. S. patent no. 6,115,403 and further in view of Alexander et al. U.S. patent no. 5,784,184 and Taga et al. U. S. patent no. 5,585,954

As per claims above, Ishikawa, Aoki and Brenner disclose all the limitations except for an optical transmitter include an encoder for adding a redundancy code to a transmission data code and an optical receiver include a decoder for correcting bit error of electrical signal according to redundancy code and monitor unit include counter for counting the number of corrections of bit error obtained by decoder.

Alexander et al. disclose an optical transmitter include an encoder for adding a redundancy code to a transmission data code (column 5 line 66 to column 6, line 7) and an optical receiver include a decoder for correcting bit error of electrical signal according to redundancy code (column 6, lines 7-12). It would have been obvious to an artisan at the time the invention was made to include the encoder and decoder of Alexander in the



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transmission system of Ishikawa, Aoki and Brenner in order to monitor and correct the bit error rate (column 5 line 66 to column 6, line 12).

Taga et al. disclose counter (figure 2, element 14) for counting the number of correction of bit error obtained by decoder (column 4, line 64-67). Since bit error counter is well known for counting bit errors on the signal, it would have been obvious to an artisan at the time the invention was made to include the bit error counter of Taga in the system of Ishikawa, Aoki, Brenner and Alexander in order to measure and correct the bit error rate.

### ***Conclusion***

8. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

- a. Ishizaka U.S. patent no. 5,999,298 discloses Electroabsorption optical intensity modulator
- b. Roberts U.S. patent no. 6,252,692 discloses optical fiber transmission system


9. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dzung Tran whose telephone number is (703) 305-0932.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's Supervisor, Jason Chan, can be reached on (703) 305-4729.

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The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9314.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 305-3900.



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